

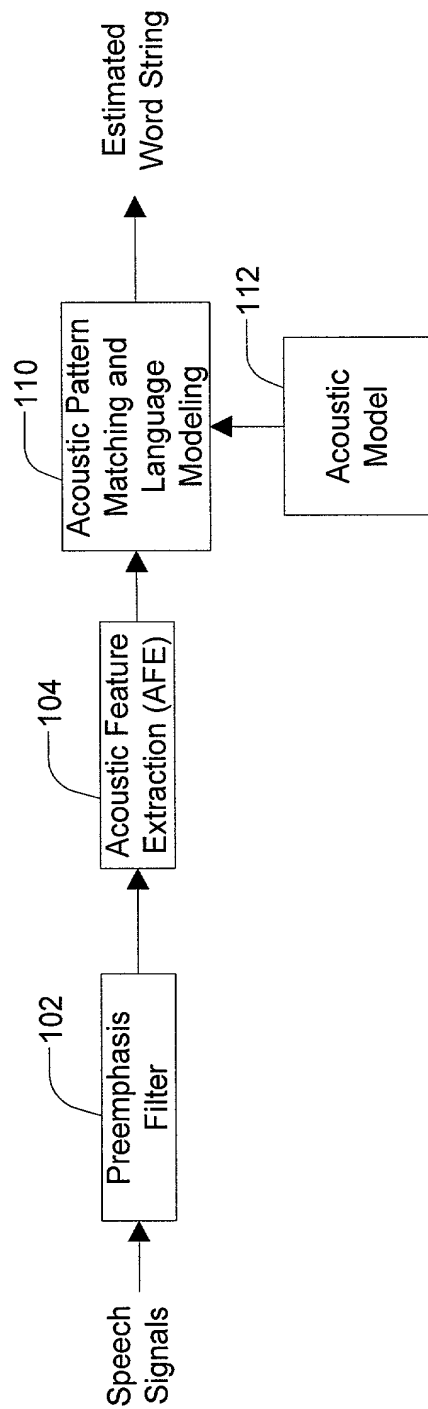
[illegible]

FIG. 2 is a block diagram of a system 200. The system 200 includes a first device 202 and a second device 204. The first device 202 includes a microphone (MIC) 210, an analog-to-digital converter (ADC) 212, a programmable logic device (PLD) 214, an analog front-end (AFE) 216, a control processor 222, a memory 227, a VR engine 220, an adaptation engine 224, an acoustic model 226, and an adaptation model 228. The second device 204 includes a modem 230, a control processor 232, a memory 236, a VR engine 234, and a central acoustic model 238. The first device 202 is connected to the second device 204 via a communication link 206.

FIG. 2

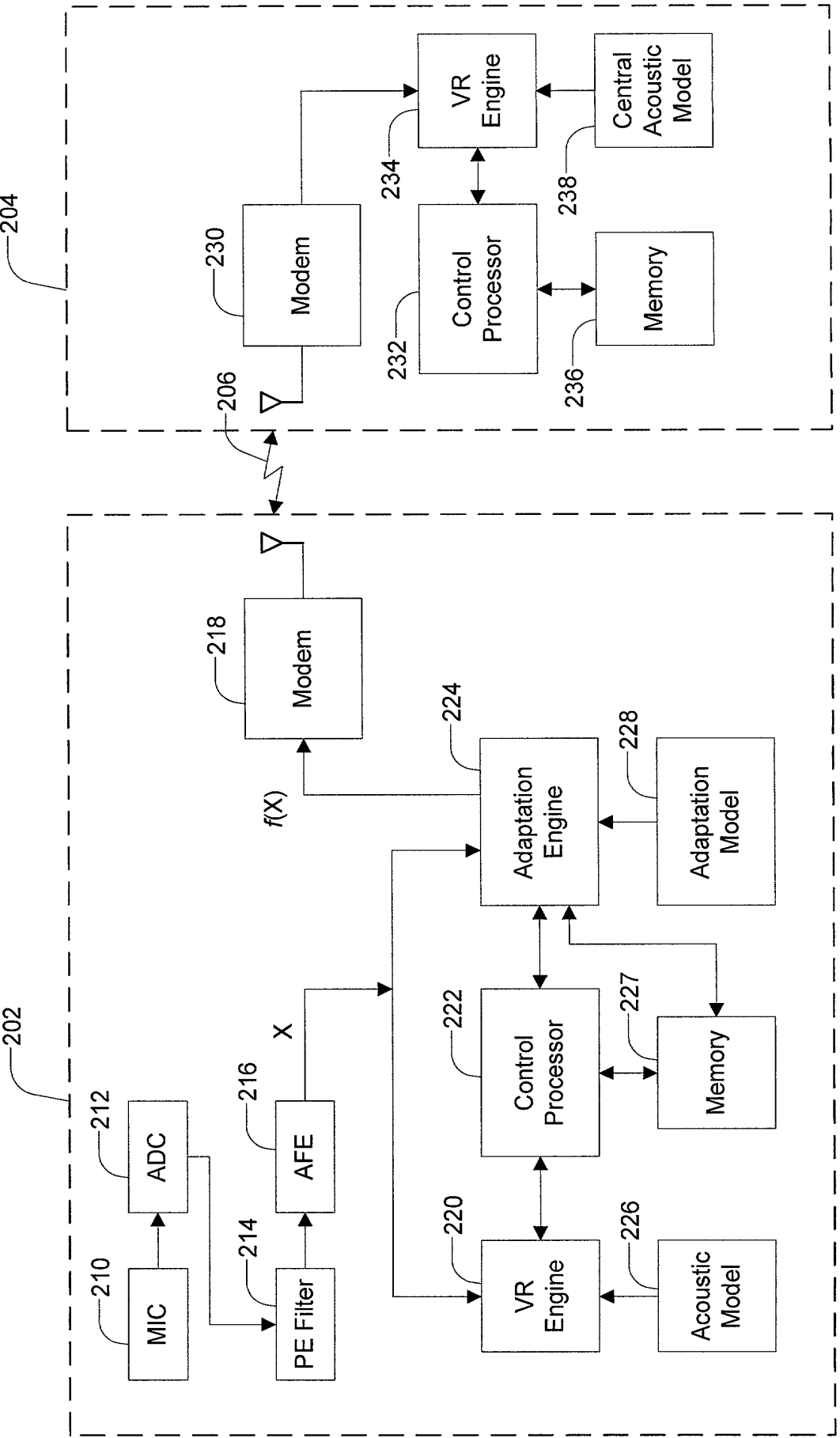


FIG. 3

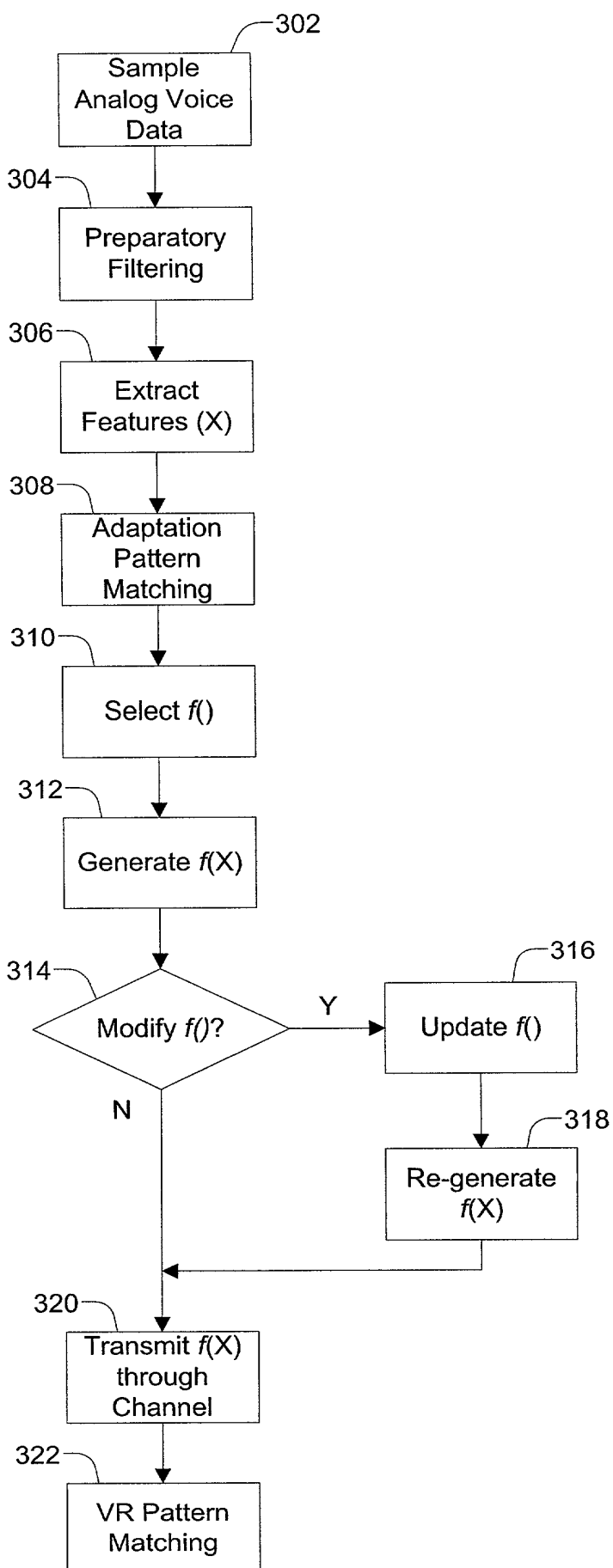


FIG. 4

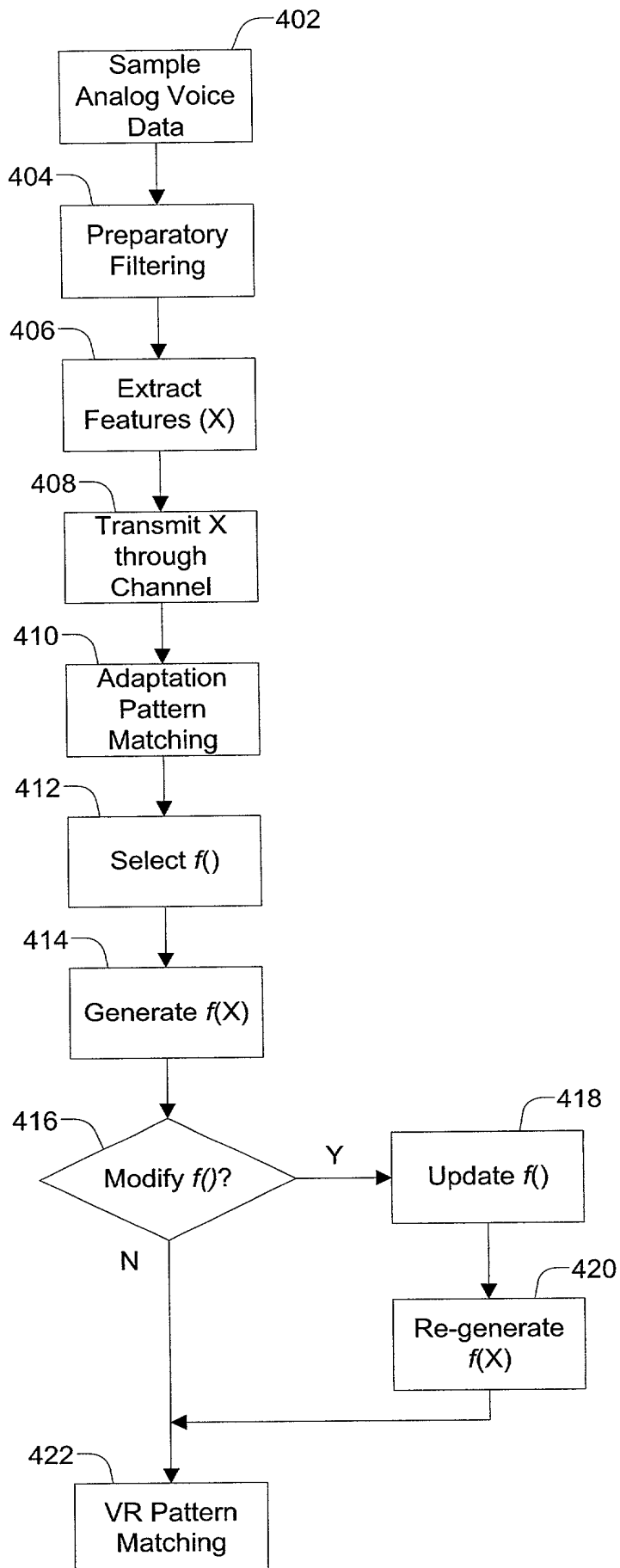


FIG. 5

